

Practitioner's Docket No. MI22-1443



PATENT

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Moore, John T.; Blalock, Guy T.; and DeBoer, Scott Jeffrey

Application No.: 09/603,147

Group No.: 2822

Filed: 06/23/2000

Examiner: Unknown

For: Capacitor Constructions, DRAM Constructions and Semiconductive Material Assemblies

Assistant Commissioner for Patents
Washington, D.C. 20231

REQUEST FOR CORRECTED FILING RECEIPT

1. Attached is a copy of the official filing receipt received from the PTO in the above application for which issuance of a corrected filing receipt is respectfully requested.
2. There is an error with respect to the following, which is omitted.

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Debbie West
Signature

Date: 10/2/00

Debbie West
(type or print name of person certifying)

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(Request for Corrected Filing Receipt—page 1 of 2)

Error in

1. Related Patent Data

Reg. No. 38,533
Tel. No.: (509) 624-4276
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Correct data

1. This patent resulted from a divisional application of U.S. Patent Application Serial No. 09/175,051, which was filed on October 19, 1998.

3. The correction is not due to any error by applicant and no fee is due.

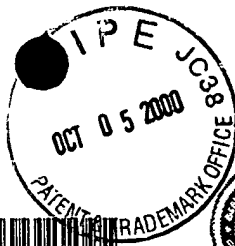


SIGNATURE OF PRACTITIONER

David G. Latwesen, Ph.D.
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601 W. First Avenue, Suite 1300
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FILING RECEIPT

OC00000005325832

**UNITED STATES DEPARTMENT OF COMMERCE
Pat nt and Trademark Office**Address: ASSISTANT SECRETARY AND
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APPLICATION NUMBER	FILING DATE	GRP ART UNIT	FIL FEE REC'D	ATTY. DOCKET NO	DRAWINGS	TOT CLAIMS	IND CLAIMS
09/603,147	06/23/2000	2822	894	MI22-1443	11	27	4

021567

WELLS ST JOHN ROBERTS GREGORY AND MATKIN
SUITE 1300
601 W FIRST AVENUE
SPOKANE, WA 992013828

Date Mailed: 08/16/2000

Receipt is acknowledged of this nonprovisional Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. **If an error is noted on this Filing Receipt, please write to the Office of Initial Patent Examination's Customer Service Center. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the PTO processes the reply to the Notice, the PTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).**

Applicant(s)John T. Moore, Boise, ID ;
Guy T. Blalock, Boise, ID ;
Scott Jeffrey DeBoer, Boise, ID ;**Continuing Data as Claimed by Applicant****Foreign Applications**

If Required, Foreign Filing License Granted 08/15/2000

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Title

Capacitor constructions, DRAM constructions, and semiconductive material assemblies

Preliminary Class

438

Data entry by : HARRISON, BRUCE

Team : OIPE

Date: 08/16/2000

